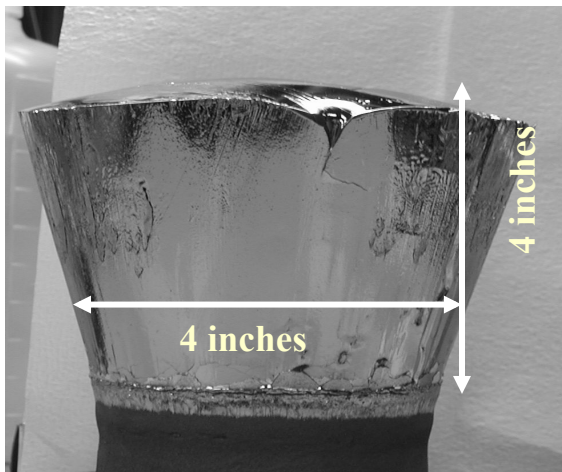


Nanoelectronics in Japan and TIA

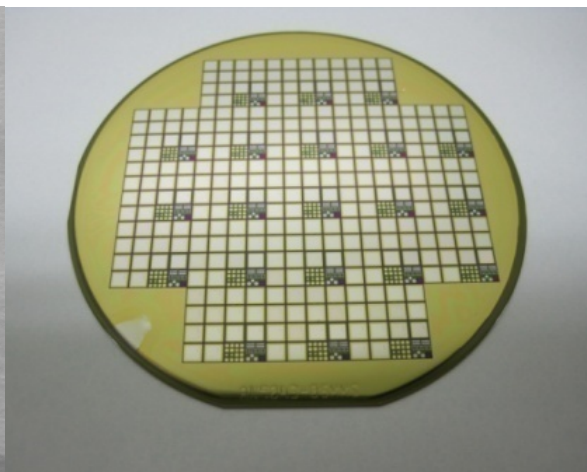
Part 5 of 7

October 17 2013
Shigeo Okaya AIST

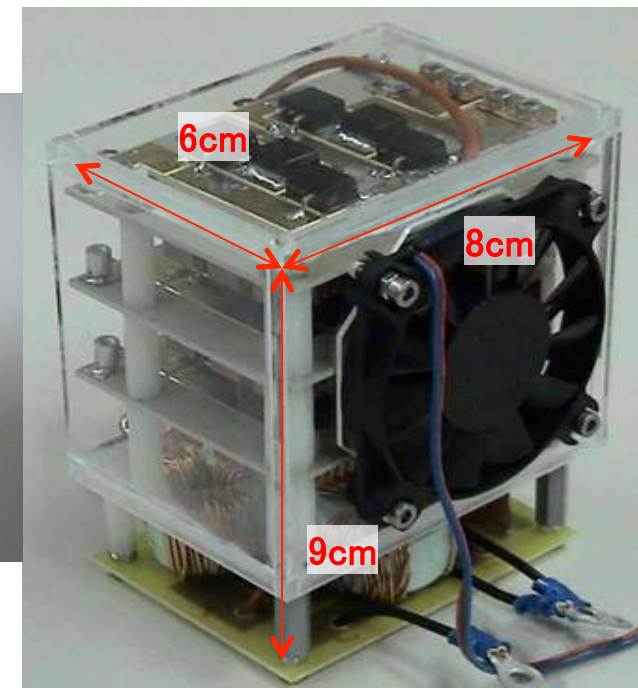
Consistent R&D on SiC devices for more than 30 years
Accelerating application for products with pilot production of SiC devices



SiC single crystal



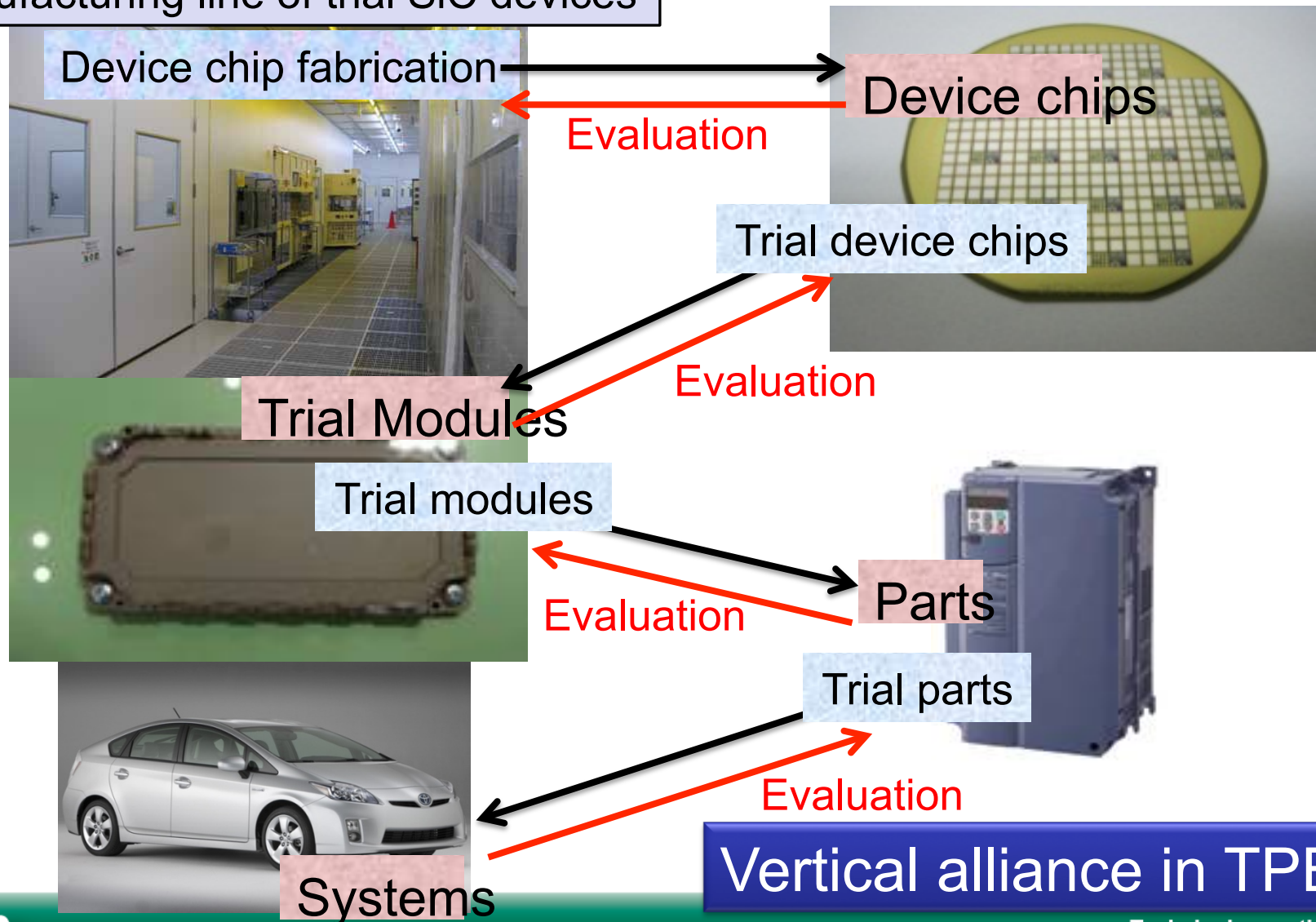
Device on SiC



Module development (inverter)

Tsukuba Power Electronics Constellation (TPEC)

Manufacturing line of trial SiC devices



Vertical alliance in TPEC

Tsukuba Power Electronics Constellation (TPEC)



Principal Member



産総研

独立行政法人産業技術総合研究所



Member



東京エレクトロン



新日鐵住金

DENSO



新日本無線



新日鉄住金マテリアルズ株式会社

Hitachi Chemical

Associate Member

TOYOTA



YAMAHA



日立国際電気

R&D cost-sharing is effective to reduce possible technological and business risks.



AIST is working as an innovation hub.

Power Electronics

Road map of SiC power electronics technology

